

FIG. 1

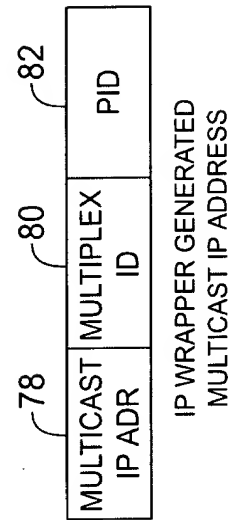


FIG. 2



PROCESS OF ENCAPSULATION CARRIED OUT IN IP WRAPPER FPGA

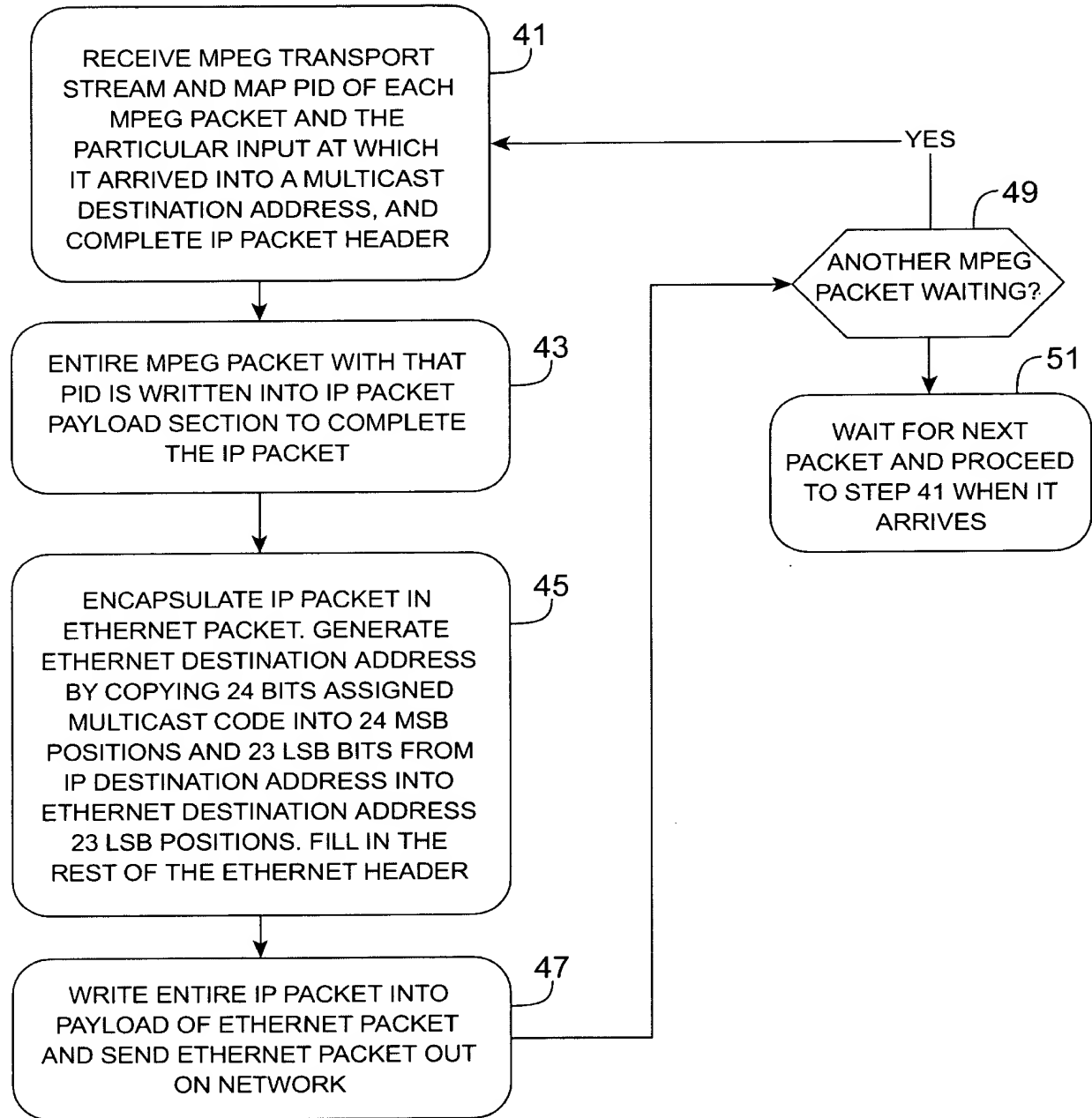
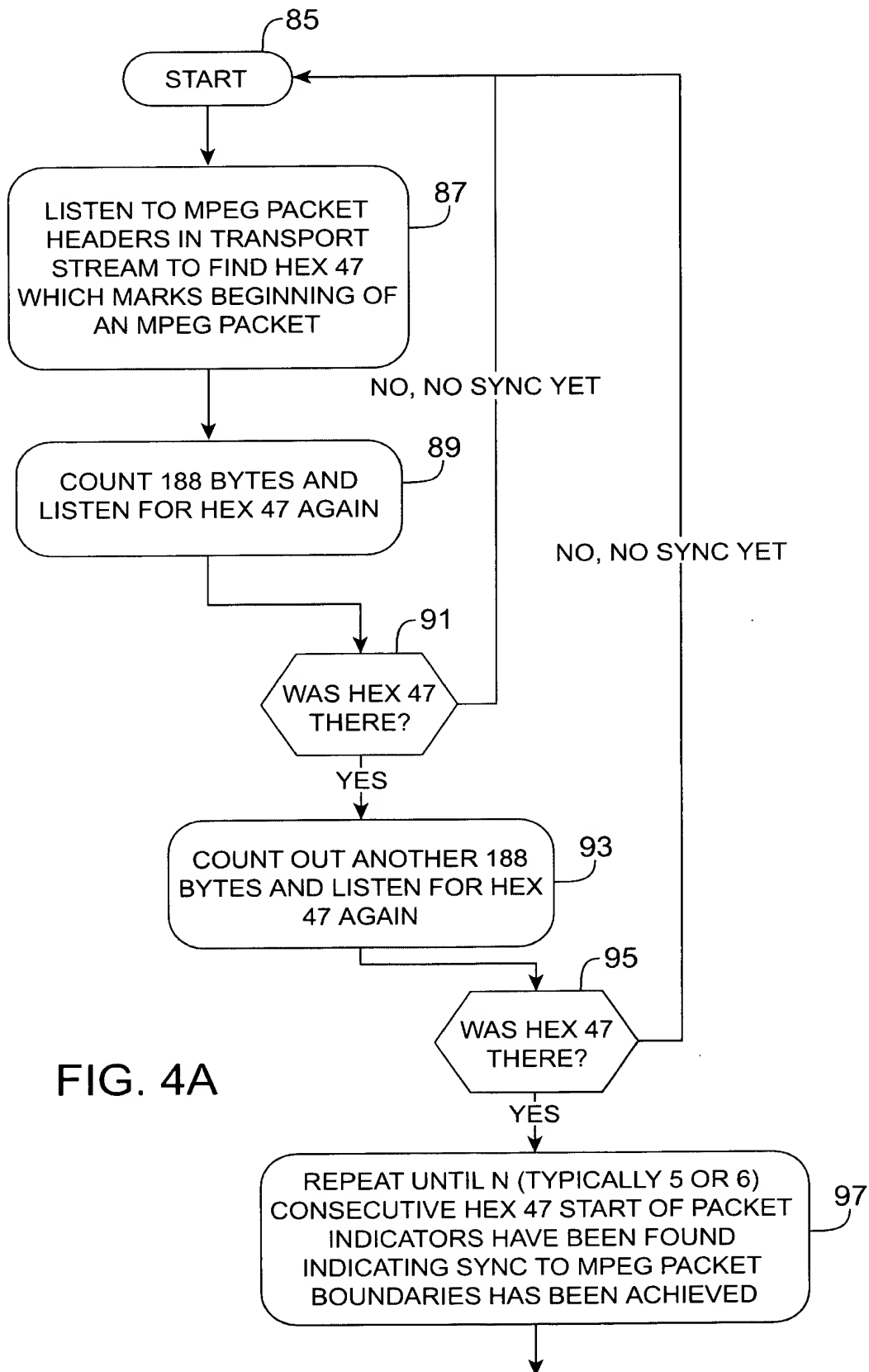


FIG. 3



PREFERRED ENCAPSULATION PROCESS CARRIED OUT BY IP WRAPPER





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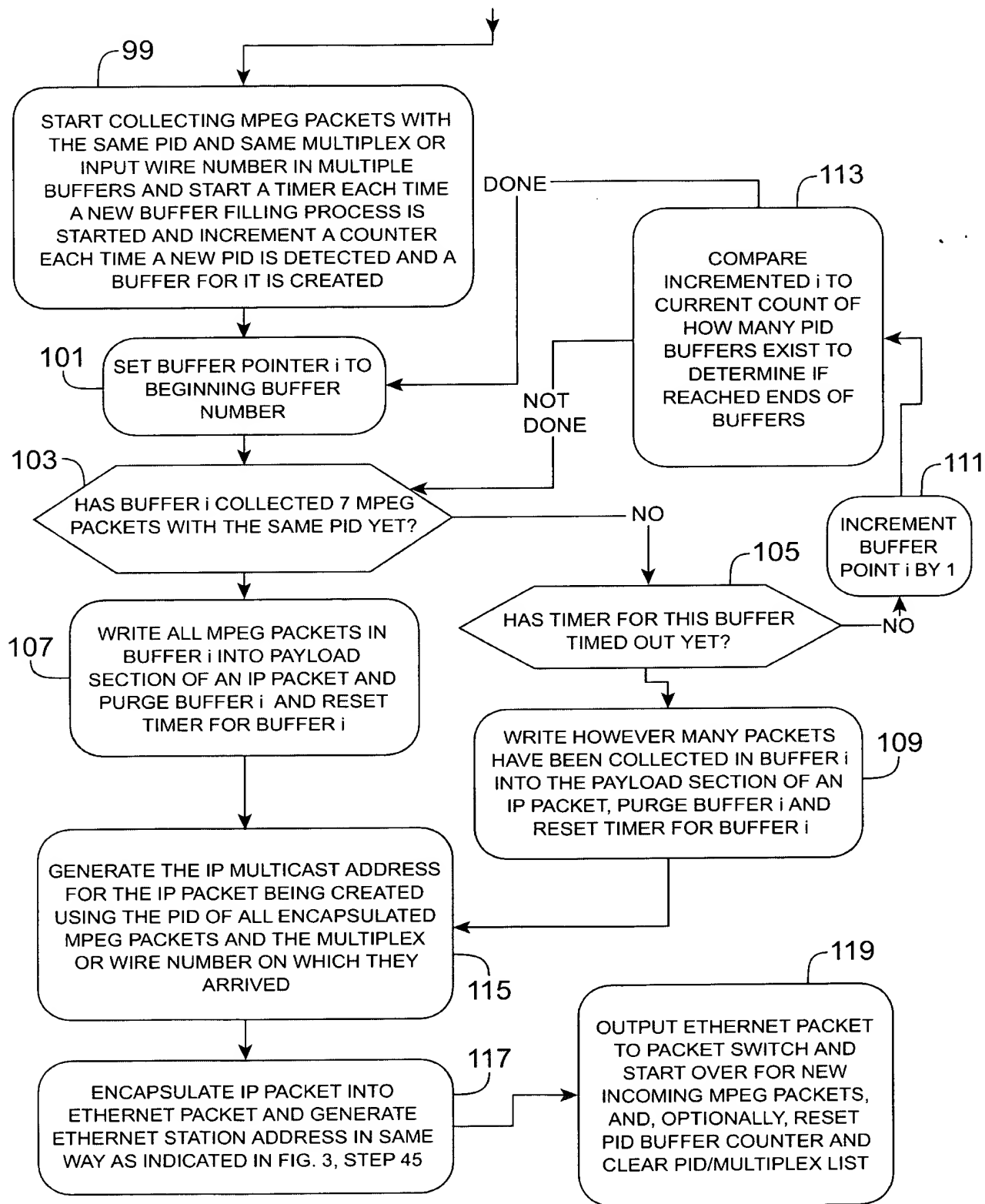


FIG. 4B



PROCESS OF GENERATING MPEG TRANSPORT STREAMS IN CHERRYPICKER SWITCHES

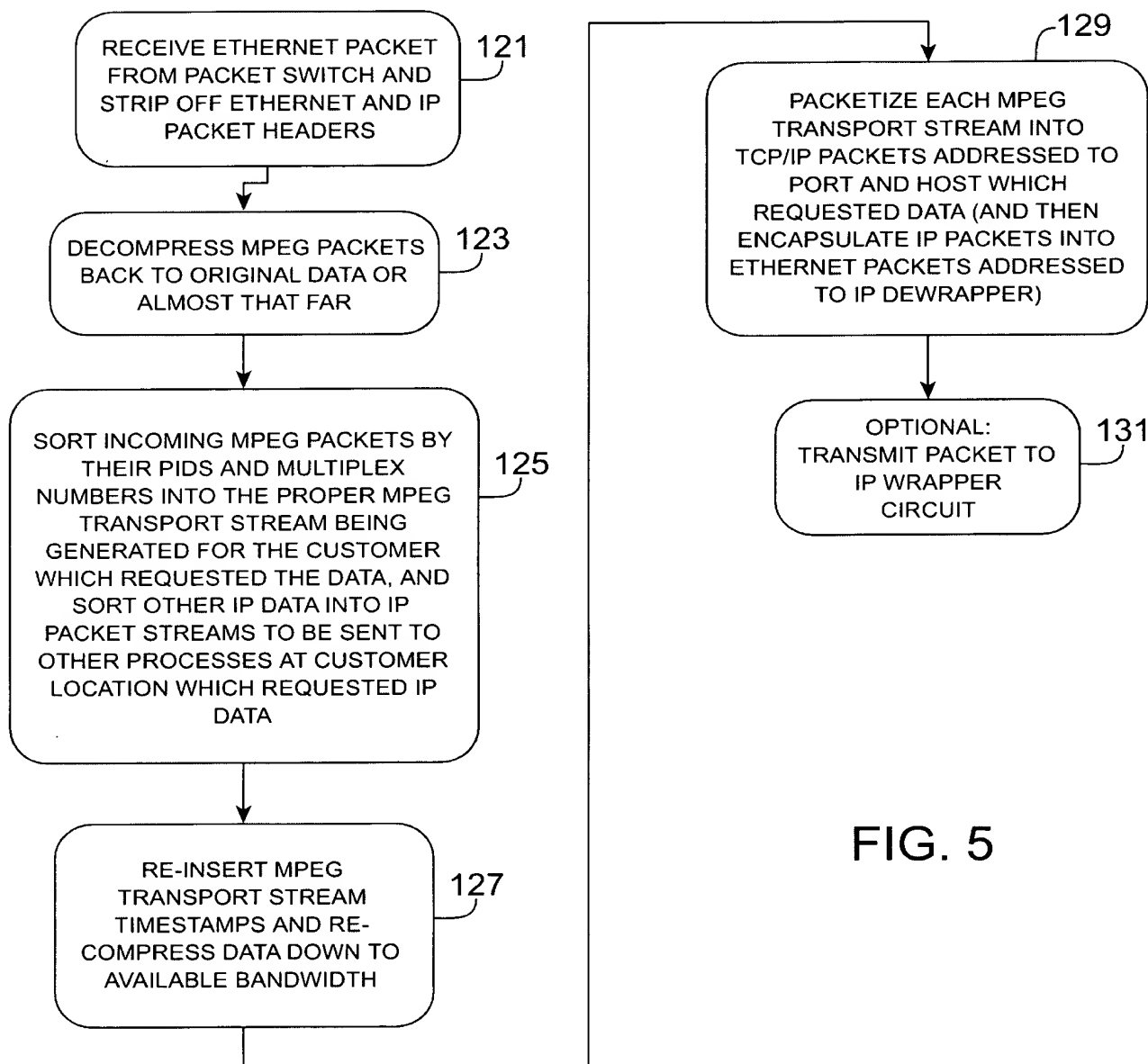


FIG. 5



IP DEWRAPPER PROCESS FOR DSL OR HFC ENVIRONMENTS

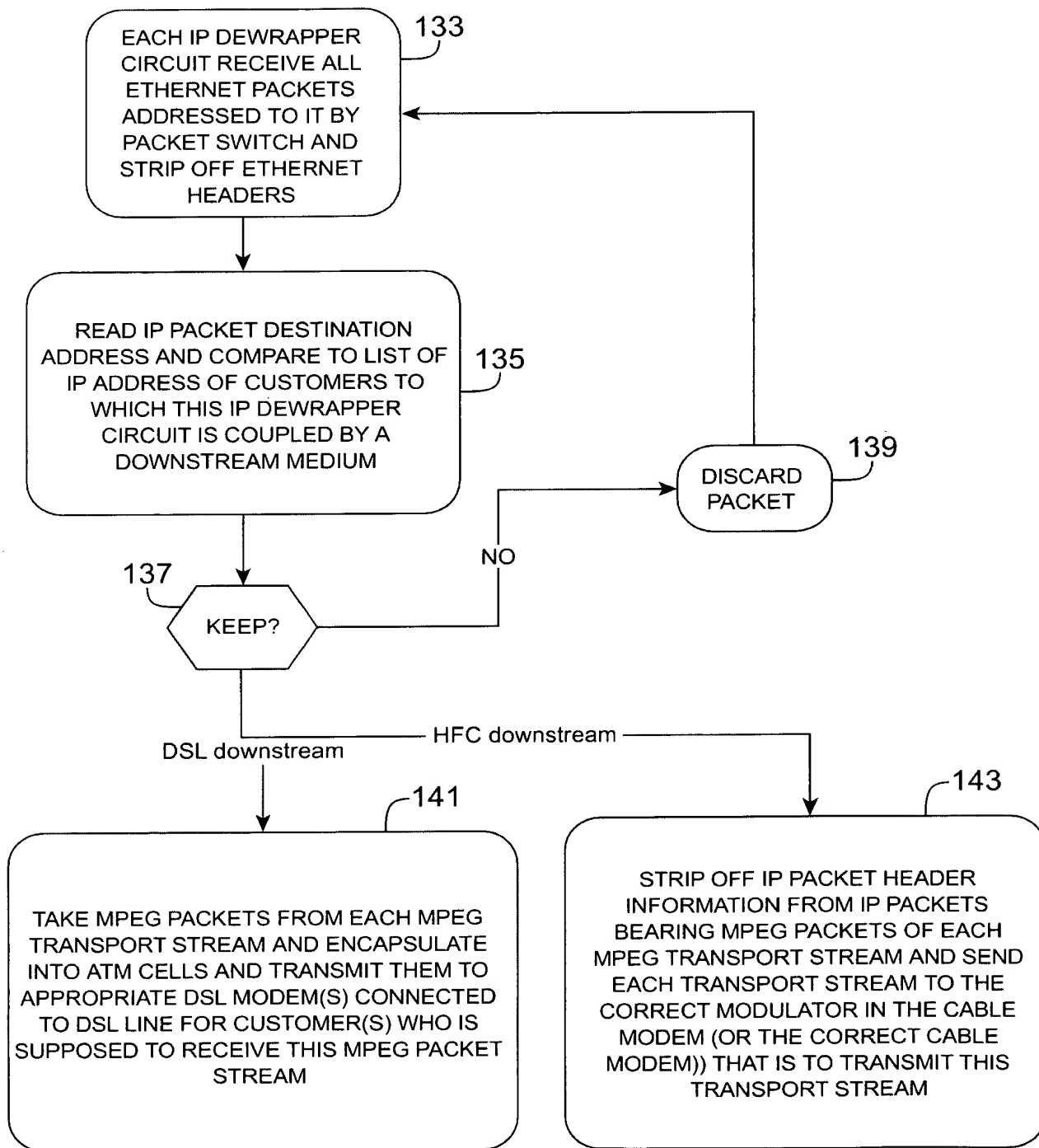


FIG. 6



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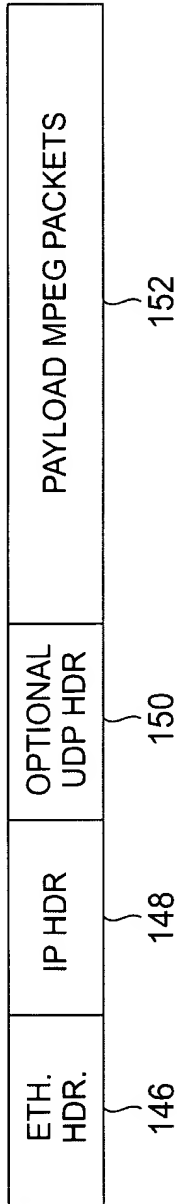


FIG. 7

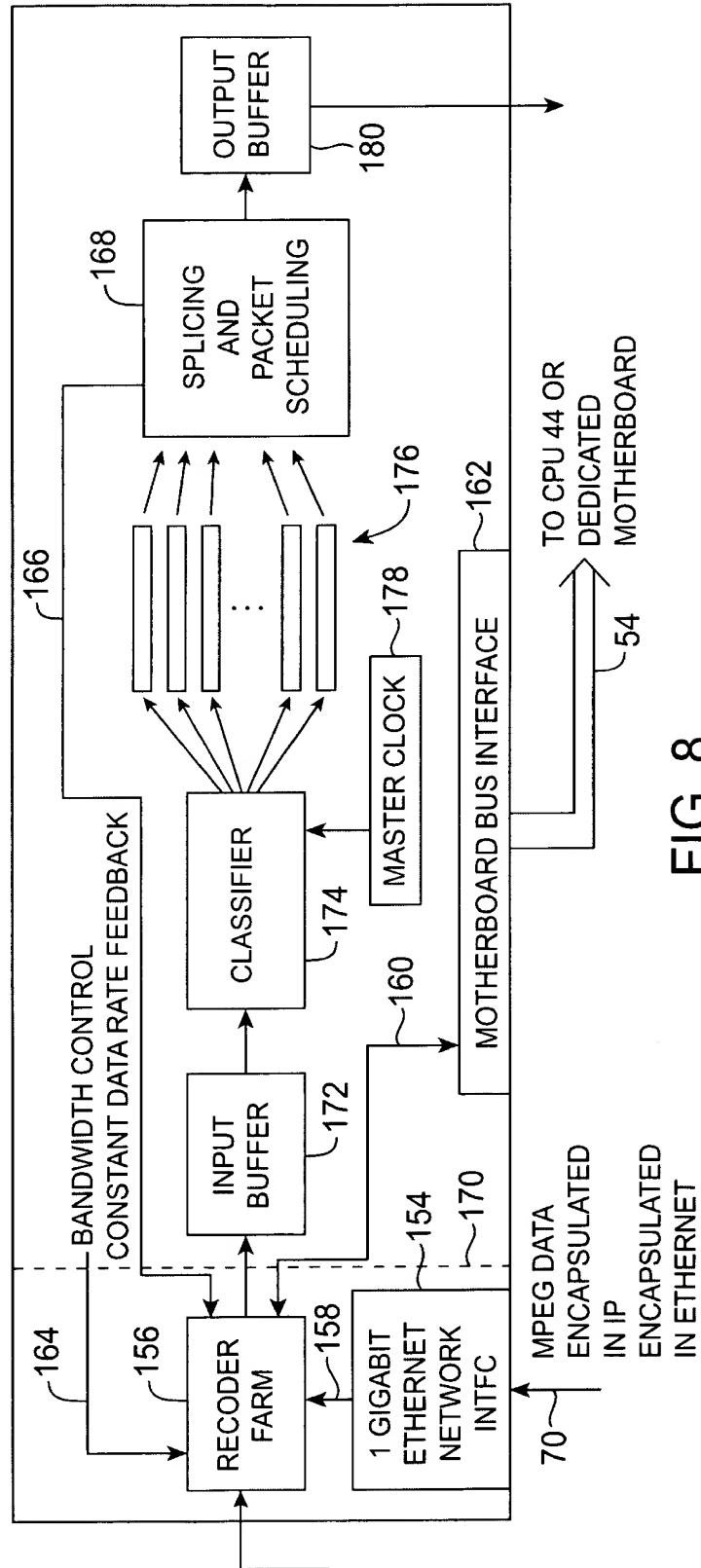


FIG. 8

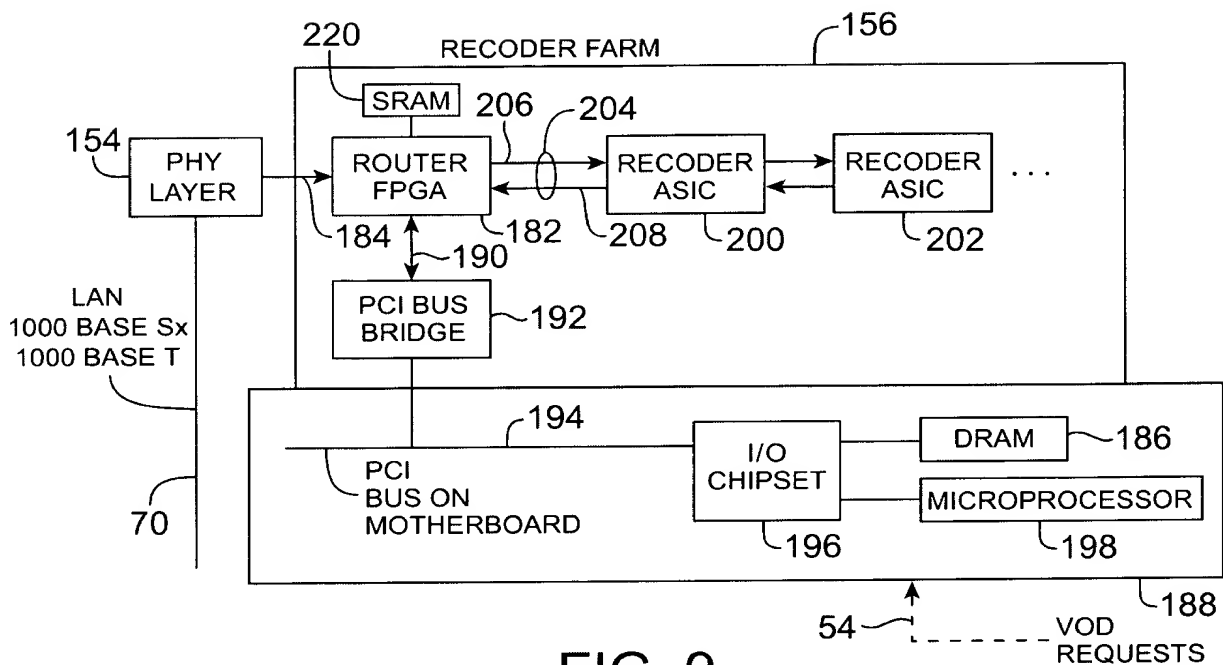


FIG. 9

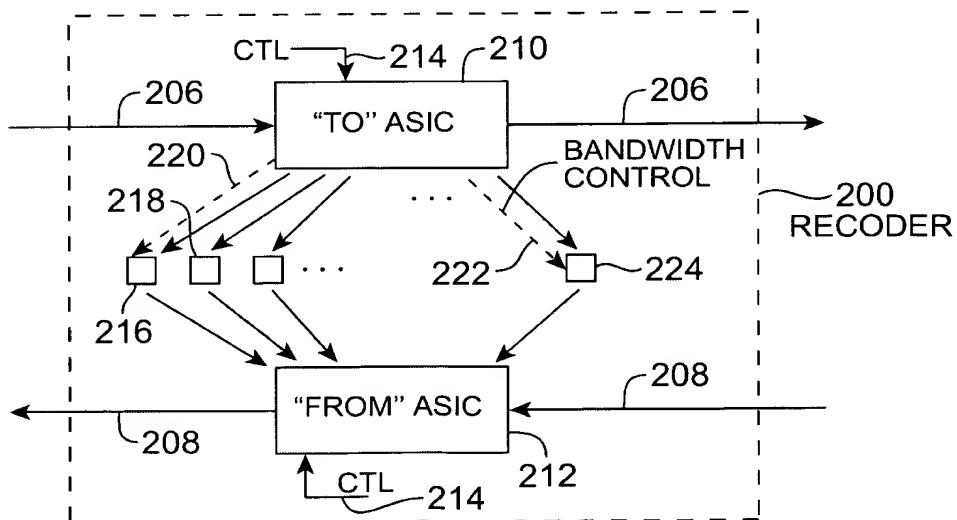


FIG. 10

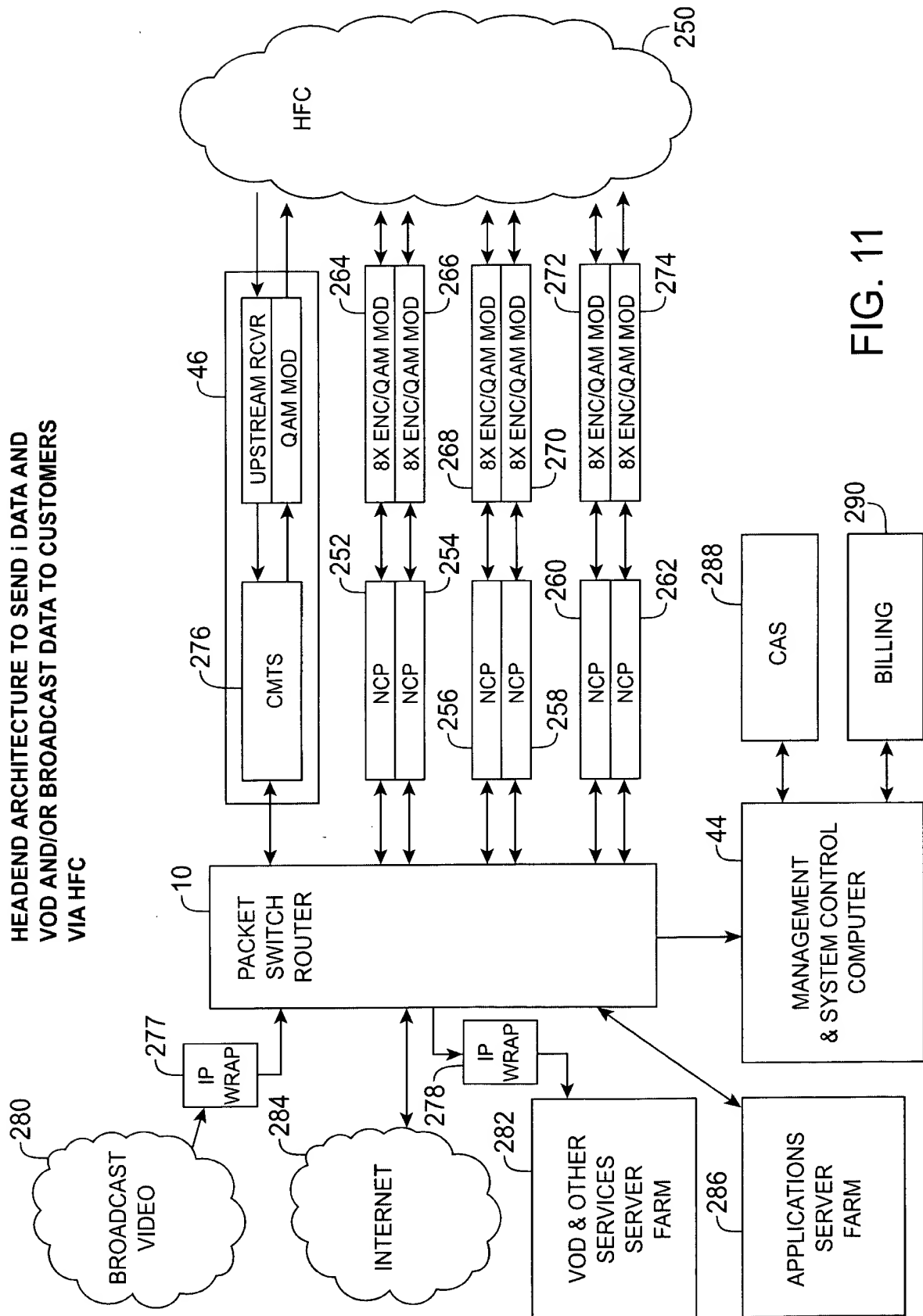


FIG. 11



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EMBODIMENT FOR TRANSMISSION OF iDATA AND VIDEO
PROGRAM DATA FROM HEADEND TO CUSTOMERS

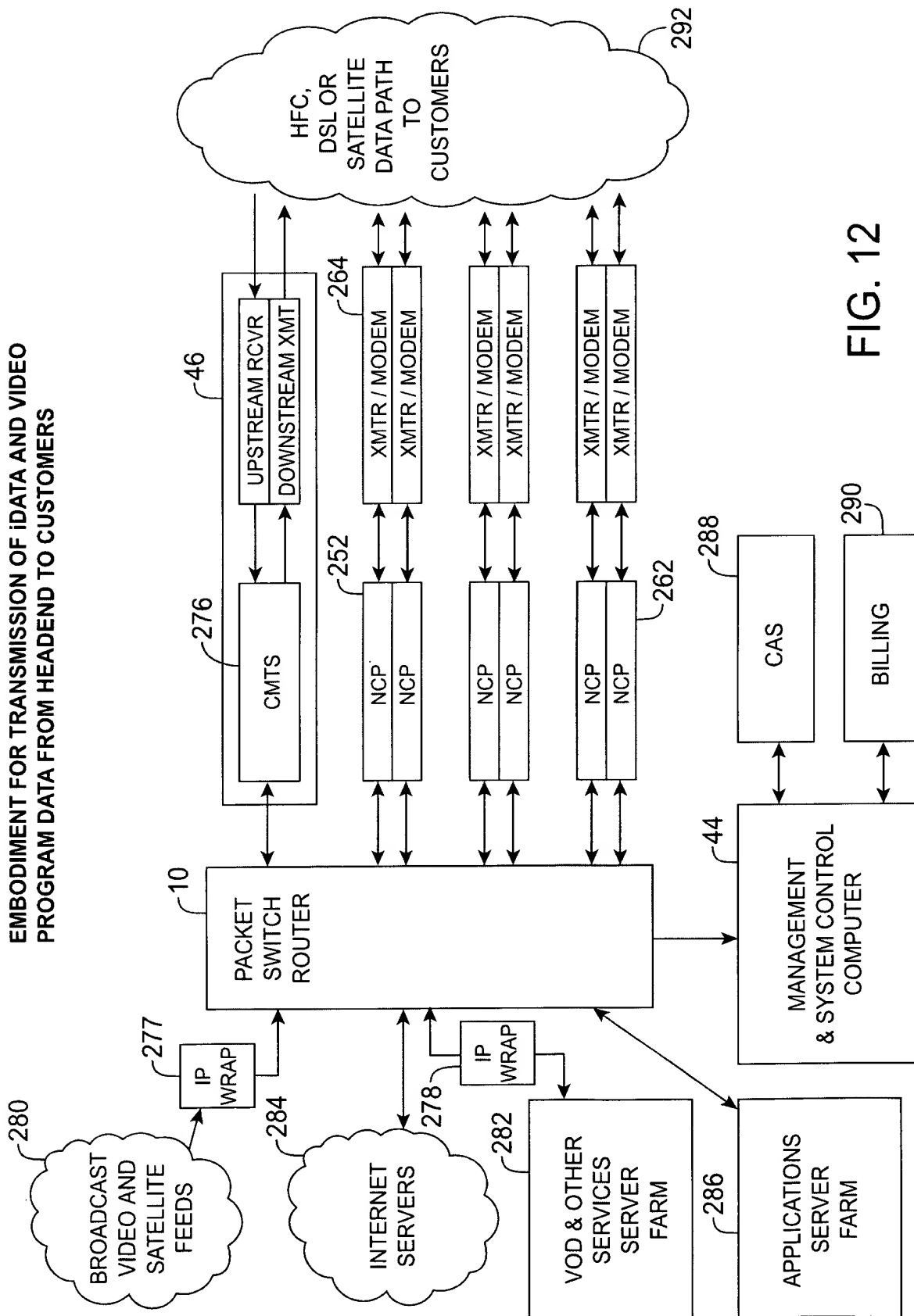


FIG. 12